



#5/Amnt A
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JLQ

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Confirmation: 2186

Koju AOKI, et al.

Art Unit: 2816

Serial No.: 10/084,974

Examiner: M. T. Ton

Filed: March 1, 2002

Docket No.: 108075-00079

For: MODE SWITCHING METHOD FOR PLL CIRCUIT AND MODE
CONTROL CIRCUIT FOR PLL CIRCUIT

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

February 27, 2003

Dear Sir:

In response to the Office Action dated September 27, 2002 (hereinafter "outstanding Action), the period for response being extended by the attached Petition for Extension of Time, please reconsider the above-identified application amended as follows:

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IN THE CLAIMS:

Please amend claims 1, 4 and 19 as follows:

- AI
1. (Amended) A method of switching the mode of a PLL circuit, wherein the PLL circuit includes a phase comparator for comparing the phase of a reference frequency-divided signal and the phase of a comparison frequency-divided signal with each other and generating a comparison output signal, a charge pump for generating a current depending on the comparison output signal from the phase comparator, and a voltage-controlled oscillator for generating an output signal having a predetermined
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